

comprises:

a first column select transistor, the first column select transistor being adapted for control by a column selector,

a first row select transistor, the first row select transistor being adapted for control by a row selector, the first select transistors being connected in series to each other and between a node and a first supply,

an output connected to the node,

a second column select transistor, the second column select transistor being adapted for control by a column selector,

a second row select transistor, the second row select transistor being adapted for control by a row selector, the second select transistors being connected in series to each other and between the node and a second supply, and

a return electrode.

A1
cancel

A2
Sub B1

17. (Amended) The circuit of claim 1 wherein the return electrode is another unit cell.